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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
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09/052,744 03/31/98 HAMDI

R CCC1P020/P15

020790 TM02/0605  
AKIN GUMP STRAUSS HAUER & FELD  
1900 FROST BANK PLAZA  
816 CONGRESS AVENUE  
AUSTIN TX 78701

EXAMINER

ELAMIN, A

ART UNIT

PAPER NUMBER

2182

DATE MAILED:

06/05/01

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JUN 05 2001

Technology Center 2100

BEFORE THE BOARD OF PATENT APPEALS  
AND INTERFERENCES

Paper No. 22

Application Number: 09/052,744  
Filing Date: 03/31/1998  
Appellant(s): HAMDI et al.

RUSSELL C. SCOTT.  
For Appellant

**EXAMINER'S ANSWER**

This is in response to Appellant's brief on appeal filed 03/26/2001.

**Real Party in Interest**

The Appellants' statement identifying the real party in interest contained in the brief is correct.

**Related Appeals and Interferences**

The Appellants' statement of the related appeals and interferences contained in the brief is correct.

Art Unit: 2182

**Status of Claims**

The statement of the status of the claims contained in the brief is correct.

**Status of Amendments After Final**

The Appellant's statement of the status of amendments after final rejection contained in the brief is correct.

**Summary of Invention**

The summary of invention contained in the brief is correct.

**Issues**

The Appellant's statement of the issues in the brief is correct.

**Grouping of Claims**

The Appellants' statement of the grouping of the claims in the brief is correct.

**Claims Appealed**

The copy of the appealed claims contained in the Appendix (pages 7-13) to the brief is correct.

*Prior Art of Record*

U.S. Patent 5,870,080 to Burnett et al. (hereinafter Burnett), filed on March 14, 1996, and issued on Feb. 9, 1999.

NEW PRIOR ART

No new prior art has been applied in this examiner's answer.

*Grounds of Rejection*

The following ground(s) of rejection are applicable to the appealed claims:

Claims 1-11, 14, 17, 18, 23 & 24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Burnett et al, US. Pat. No. 5,870,080.

As to claim 1, 2 & 23, Burnett discloses a computer system comprising;  
a computer including at least a USB port [Port 111 of Fig. 4];  
a first transceiver coupled to the USB port of said computer [Transceiver 131 of Fig. 4]; and  
a second transceiver coupled to a peripheral device [col. 4, lines 39-41];  
said first and second transceivers cooperate to form a wireless USB bus link between said computer and said peripheral device [Abstract].

**Burnett** does not explicitly teach that said second transceiver is coupled to the USB port of a peripheral hub device. Burnett explicitly teaches that the second transceiver is coupled to the USB port of a peripheral device. “While Fig. 1 illustrates the invention using a printer, the use of other similarly equipped peripheral devices will be apparent to those skilled in the art”, col. 4, lines 63 - 65. The examiner submits Burnett et al clearly teaches the usage of any type of USB peripheral devices including the claimed peripheral hub device to house the second transceiver because the claimed peripheral hub device is a well-known USB peripheral device.

As to claim 3, it is a matter of design choice to have said first transceiver internal to the housing of said computer.

As to claim 4, Burnett teaches said computer has a housing and wherein said first transceiver is provided external to the housing of said computer, and said transceiver couples to the USB port of said computer [Figs 1 & 4].

As to claim 5 & 10, Burnett teaches said peripheral device is a printer [Fig. 1]

As to claims 6 & 7, it is a matter of design choice to have said second transceiver provided internal or external to the peripheral housing.

As to claims 8 & 9, it is well known in the art to have said peripheral device being a peripheral hub having a plurality of USB ports, and having said peripheral hub further includes

an upstream USB port, and having said transceiver being connected to the upstream USB port of said peripheral hub for the reasons set forth hereinabove.

As to claims 11, 14, 17 & 18, Burnett teaches said computer supplies a wired internal USB bus to the USB port of said computer [Fig. 4], and wherein first transceiver comprises:

a first transceiver circuit [Transceiver 131 of Fig. 4];

a first bus interface that interfaces said transceiver circuitry to the internal USB bus [Fig. 4], and

an antenna operatively coupled to said transceiver [col. 3, lines 4-10].

As to claim 24, managing power utilization of transceivers is well known in the art.

#### **Response to Argument**

The Examiner summarizes the various points raised by the Appellants and addresses replies individually.

In the arguments, the Appellant argues in substance that:

Barnett does not teach, disclose, or otherwise suggest the following limitations:

- a-* a peripheral hub device including at least a USB Host port;
- b-* a radio frequency transceiver apparatus for providing a wireless USB

**In response to a;**

Burnett's explicitly teaches that any peripheral devices can be used to house the second transceiver [col. 4, lines 63 - 65]. The examiner submits that Burnett's any type of USB peripheral devices encompassing the claimed peripheral hub device because the peripheral hub device is a well-known USB peripheral device.

**In response to b;**

Burnett teaches using radio frequency transceiver [*see col. 5, lines 1-2*].

For the above reasons, it is respectfully submitted that the rejections should be sustained.

Respectfully Submitted,

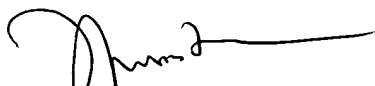
Abdelmoniem Elamin

May 30, 2001

Conferee

 **MENG-AL T. AN**  
**SUPERVISORY PATENT EXAMINER**  
**TECHNOLOGY CENTER 2100**

Meng An

  
Thomas Lee